



APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/510,298	10/06/2004	Johannus Wilhelmus Weekamp	NL 021102	6543
24737 7590 01/18/2007 PHILIPS INTELLECTUAL PROPERTY & STANDARDS P.O. BOX 3001 PRIADCH FE MANOR NV 10510			EXAMINER	
			GETACHEW, ABIY	
BRIARCLIFF MANOR, NY 10510		ART UNIT	PAPER NUMBER	
·			2841	
SHORTENED STATUTOR	Y PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE	
3 MO	NTHS	01/18/2007	PAPER	

# Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

	Application No.	Applicant(s)				
Office Action Summary	10/510,298	WEEKAMP, JOHANNUS WILHELMUS				
·	Examiner	Art Unit				
	Abiy Getachew	2841				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period w  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tim rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1)⊠ Responsive to communication(s) filed on 13 No	ovember 2 <u>00</u> 6.	•				
	<u> </u>					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4)⊠ Claim(s) <u>7-11</u> is/are pending in the application.						
4a) Of the above claim(s) <u>1-6</u> is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6) Claim(s) 7-11 is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or	election requirement.					
Application Papers						
9) The specification is objected to by the Examiner	•					
10)⊠ The drawing(s) filed on <u>06 October 2004</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) ☐ All b) ☐ Some * c) ☐ None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview Summary Paper No(s)/Mail Da					
3) Notice of Informal Patent Application						
Paper No(s)/Mail Date <u>06/27/2005</u> . 6) Other:						

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#### **DETAILED ACTION**

### Election/Restrictions

1. Applicant's election without traverse of Group I, claims 7-11, drawn to Electronic Device, classified in class 174 Subclass 261. In the reply filed on 11/13/2006 is acknowledged.

Claim s 1-6 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected to method for installing a removable panel to portable electronic device, classified in class 29 subclass 852. There being no allowable generic or linking claim. Election was made **without** traverse during the phone conversation with Michael E. Marign on 11/13/2006.

The restriction requirement is deemed proper and made **final**.

## Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 7-11 rejected under 35 U.S.C. 102(b) as being anticipated by Makamura et.al. (6,882,544 B2)

Regarding claim 7, Makamura et.al. discloses an electronic device (10) comprising a substrate (130) with an electrical element (14) on its first side which

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element comprises a first (20) and a second electrode (21) which are connected to a metallization (13) by electrical contacts (112) and electrodes (20,21) of further electrical elements (14), the electronic device (10) further comprising an inductive element (120), characterized in that the inductive element (120) is defined in an electroconductive (120) patterned layer (See figure 7) which is located on the first side of the substrate (130) and which is electroeonductively connected to the metallization (11), isolating (15) material being present between said substrate and the patterned layer in which isolating (115) material the pattered layer (120) has been mechanically embedded.

In regards claim 8 as applied the above claim Makamura et.al. discloses, characterized in that the patterned (120) layer extends in a plane parallel (See figure 70 to the first side beyond the substrate (130) and contact pads (105) for external contacting of the electronic device and the isolating material in essence completely envelops the substrate (130). (See the abstract)

[Column 14 paragraph 2 lines 6-35] [Column 13 paragraph 3 lines 19-26]

In regards claim 9 as applied the above claim Makamura et.al. discloses, characterized in that the patterned (120) layer is situated between the isolating material (15) and additional layers (130) in which vertical interconnect areas (vias) (19) are defined. (See figure 7) [Column 15 paragraph 11 lines 47-52]

In regards claim 10 as applied the above claim Makamura et.al. discloses, characterized in that the metallization (11) along a boundary face with the isolating material (15) includes an inductive element (12) which is situated substantially opposite

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the inductive element (12) in the patterned layer (120), the inductive elements (12) together forming a strip line. (See figure 7)

In regards claim 10 as applied the above claim Makamura et.al. discloses, characterized in that the substrate (130) comprises a semiconductor material (See figure 7) selected from the group of III-V and II-VI connectors and in that the electrical element (14) is a semiconductor element. (Semiconductor elements mounted on the circuit substrate 130) (The Group III-V compound semiconductor contains a Group III element and a Group V element of the Periodic Table as principal components, and contains also 0.1 atom % to 40 atom % of hydrogen atoms and 100 ppm to 20 atom %, based on the sum of the atomic numbers of the Group III element and the Group V element, of at least one element selected from among Be, Mg, Ca, Zn and Sr.)

#### Correspondence

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Abiy Getachew whose telephone number is (571) 272 6932. The examiner can normally be reached on Monday to Friday 8Am to 4:30Pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Reichard Dean can be reached on (571) 272-1984. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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A.G.